

*OK to enter. 10/24/00 JH*

Attorney Docket No. 10030374-1

**In the Specification:**

[29] The BIST sequence controller is coupled to control line ~~44~~40 for providing control signals to the SERDES circuits to enable the SERDESs and set the loop mode, for example. The BIST sequence controller also provides the error detection signal over line **46**.